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INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

Application Number	10789311
Filing Date	2004-02-27
First Named Inventor	Hans Eberle
Art Unit	2136
Examiner Name	Johnson, Carlton
Attorney Docket Number	6000-31500

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/CJ/	3	20040158597			Ye, et al.
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/CJ/	1	U.S. Application Serial No. 10/387,007 entitled "Hardware Accelerator for Elliptic Curve Cryptography".	☐
	2	U.S. Application Serial No. 10/387,008 entitled "Generic Modular Multiplier Using Partial Reduction".	☐
	3	U.S. Application Serial No. 10/387,009 entitled "Modular Multiplier".	☐

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/CJ/	4	U.S. Application Serial No. 10/387,104 entitled "Generic Implementaion of Elliptic Curve Cryptography Using Partial Reduction".	☐
	5	ERDEM, et al., "A Less Recursive Variant of Karatsuba-Ofman Algorithm for Multiplying Operands of Size a Power of Two," Proceedings of the 16th IEEE Symposium on Computer Arithmetic (ARITH-16'03), June 15-18, 2003.	☐
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/CJ/	26	Orlando, et al., August 2000, "A High-Performance Reconfigurable Elliptic Curve Processor for GF(2m)," CHES 2000 Workshop on Cryptographic Hardware and Embedded Systems, Springer-Verlag, Lecture Notes in Computer Science, 1965, pp. 41-56.	☐
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